

REMARKS

Applicants present claims 22 to 44 for examination. Claim 22 is independent. Favorable reconsideration and further examination are respectfully requested.

In the Office Action, the title and claim 36 were objected to for the reasons noted on page 2. As shown above, Applicants have amended the title and claim 36. In view of these amendments, withdrawal of the objections is respectfully requested.

Turning to the rejections, claims 22 to 24, 26 to 29, 31, 32 and 41 to 44 were rejected over WO00/57515 (Kodim) in view of DE3626800 (Siemens AG); claim 25 was rejected over Kodim in view of Siemens AG and further in view of JP2-162744 (Hitachi LTD); claim 30 was rejected over Kodim in view of Siemens AG and further in view of US 5,122,921 (Koss); claims 33, 35 and 39 were rejected over Kodim in view of Siemens AG and further in view of US 6,272,327 (Kurchuck); claim 34 was rejected over Kodim in view of Siemens AG and further in view of JP02000134945 (Toshiba); claims 36 to 38 were rejected over Kodim in view of Siemens AG, Kurchuk, and US 5,276,422 (Ikeda); and claim 40 was rejected over Kodim in view of Siemens AG and further in view of US 6,072,993 (TriKha).

For at least the reasons set forth below, Applicants respectfully traverse the rejection of independent claim 22. In this regard, claim 22 is directed to circuitry comprising a terminal for use with a high-frequency signal, at least two signal lines, a switching unit for connecting the terminal to a signal line, and a primary protection device for protecting against electrostatic discharges. The primary protection device is between the terminal and the switching unit, and

the primary protection device comprises a first element that diverts voltages having a pulse height greater than 200V to a reference potential.

The applied art is not understood to disclose or to suggest the foregoing features of claim 22, particularly with respect to its primary protection device. In this regard, page 3 of the Office Action admits that Kodim does not teach a primary protection device that diverts a voltage greater than 200V to a reference potential. The Siemens AG reference was therefore cited for its alleged disclosure of this feature. In particular, it was said on pages 3 and 4 of the Office Action that the Siemens AG reference shows a primary protection device that “diverts voltages having a pulse height greater than 300V”, and that it would have been obvious to substitute the protection device of the Siemens AG reference for the protection device described in Kodim, and thereby render obvious the invention of claim 22. Applicants respectfully disagree.

More specifically, it is elemental that there must be some motivation in the references themselves or in the knowledge available to those of skill in the art to combine two references in support of an obviousness rejection. Applicants submit that there was no such motivation here. In this regard, Kodim describes using a high-pass filter as its electrostatic discharge (ESD) protection device 1 (see, e.g., Fig. 2 and page 5, lines 8 to 24 of Kodim). Kodim's ESD protection device is thus configured to pass high frequency signals; therefore, Kodim's ESD protection device is frequency-sensitive. Kodim's ESD protection device is designed to be frequency-sensitive because it addresses problems associated with receiving high-frequency cellular telephone signals (e.g., 900 MHz to 1900 MHz) (see page 1 of Kodim: “The invention relates to an electric filter for filtering low frequencies in order to protect an electronic circuit

from high voltage pulses”). By contrast, the Siemens AG protection device is not frequency-sensitive, but rather diverts voltages based on their magnitude alone “over a very wide frequency band” (see the Siemens AG English-language Abstract). Furthermore, Applicants note that the Kodim reference suggests modifications to its ESD protection device, but only modifications that preserve the high pass characteristics of the ESD device (see, e.g., Kodim, page 5, lines 17 to 20). Therefore, since the Siemens AG reference is not frequency-sensitive, Applicants submit that there is no motivation whatsoever to incorporate the Siemens AG protection device into the Kodim circuitry, as suggested in the Office Action.

Accordingly, Applicants submit that the combination of Kodim and the Siemens AG reference is improper as a matter of law, and respectfully request withdrawal thereof. Claim 22, and the claims that depend therefrom, are thus believed to be patentable.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as an intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicants: Christian Block, et al.
Serial No. : 10/526,278
Filed : March 1, 2005
Page : 11 of 11

Attorney's Docket No.: 14219-079US1
Client Ref.: P2002,0828USN

In view of the foregoing amendments and remarks, Applicants respectfully submit that the application is in condition for allowance, and such action is respectfully requested at the Examiner's earliest convenience.

Finally, Applicants submit herewith a Supplemental Declaration. In the Declaration currently on file, the "is attached hereto" box was checked post-signing.

Applicants' undersigned attorney can be reached at the address shown below. All telephone calls should be directed to the undersigned at 617-521-7896.

Please apply any fees or credits due in this case, which are not already covered by check, to Deposit Account 06-1050 referencing Attorney Docket No. 14219-079US1.

Respectfully submitted,

Date: March 28, 2006



Paul A. Pysher
Reg. No. 40,780

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

Description

Circuit Arrangement

5 The present invention relates to a circuit arrangement having a switching unit, which is connected to a terminal for a high-frequency signal. Moreover, the switching unit is connected to additional signal leads. The terminal of the circuit arrangement is connected to a protection device against electrostatic discharges.

10 Circuit arrangements of the type mentioned above are often used as multiband frontend modules for mobile telephones. In this application, they are connected to the antenna of the mobile telephone at the antenna input. If a user carrying an electrical charge contacts the antenna, this may result in an electrostatic discharge (ESD). Such electrostatic discharges may produce voltage spikes capable of destroying the circuit
15 arrangement. Consequently, it is necessary to equip circuit arrangements of the type mentioned above with a protection device against ESD.

 In high-frequency sections of mobile telephones, among other things, components are used that are sensitive to electrostatic discharges. Such components are, for example,
20 surface acoustic wave filters, gallium arsenide switches, PIN diodes, amplifiers or the like. They may be irreversibly destroyed by the action of high-frequency high voltage pulses such as those produced by ESD. These problems are just as relevant to discrete

gallium arsenide switches and frontend modules with integrated gallium arsenide switches as they are to frontend modules with pin switching technology and integrated surface wave filters. These problems also concern the ESD-sensitive components that are used in the transmission/reception paths of the high-frequency section. This complicates

5 use in mobile telephones, as a result of which electrostatic discharges directly to the antenna input of the gallium arsenide switch or frontend module are possible in external, accessible antennas, for external antennas on a car. Many manufacturers of mobile telephones therefore require that the frontend modules or gallium arsenide switches have ESD resistance in compliance with standard IEC61000-4-2 in the magnitude of 8 kV at

10 the system level.

Circuit arrangements of the type mentioned above that are equipped with a protection device against ESD are known from the publication WO 00/57515. The protection device is designed as an electric high pass filter, to which a capacitor is

15 connected in series with the antenna input path and an inductance is connected in parallel.

The high pass filter used in the known circuit arrangement to protect the circuit arrangement has the disadvantage that the function of the protective element is frequency-dependent. All frequency components of a signal are passed through nearly

20 unhindered from a specific limit frequency. All other frequency components are suppressed. The result of this frequency-dependent operating mode is that very many frequencies that are not desired in a mobile telephone are still allowed through. For

example, frequencies between approximately 1 and 2 GHz are used in mobile telephones according to the GSM, PCN or PCS standard. All other frequencies received by the antenna are likely to be interfering and must therefore be filtered out.

5 It is, therefore, the object of the present invention to specify a circuit arrangement in which the protection device against electrostatic discharges is enhanced.

This objective is achieved by a circuit arrangement according to claim 1.

Additional advantageous embodiments of the circuit arrangement may be obtained from
10 the dependent claims.

A circuit arrangement is specified which has a terminal for a high-frequency signal. Furthermore, the circuit arrangement has at least two additional signal leads. In addition, a switching unit is provided which is used to connect the terminal to one of the
15 signal leads. Furthermore, a primary protection device is provided which is connected between the terminal and the switching unit. The protection device includes a voltage limitation element which diverts voltage pulses exceeding a switching voltage to a reference potential.

20 The circuit arrangement has the advantage that the diversion of the interfering signals caused by ESD is no longer frequency-dependent. Instead, the interfering signals are detected based on their voltage elevation and diverted to the reference potential. To

that end, it is advantageous to use a voltage limitation element having a very low resistance above a switching voltage. Below the switching voltage, the voltage limitation element has a very high resistance so that signals below the switching voltage pass unhindered from the terminal to the switching unit and can be connected to a signal lead
5 from there. On the other hand, voltage pulses whose voltage elevation is greater than the switching voltage are detected reliably and diverted to the reference potential.

It is thus possible to advantageously prevent voltage pulses that are not in the useful range of the circuit arrangement and have a very high, interfering voltage elevation
10 from reaching the switching unit unhindered.

A suitable selection of the switching voltage can make it possible to divert to the reference potential only those signals that are greater than those of the circuit arrangement, for example, signals used to transfer information. This makes it possible to
15 reliably block interfering signals whose voltage elevation exceeds the carrying capacitance or the maximum intended load of the circuit arrangement on reaching the switching unit.

The reference potential may be a ground potential, for example. However, it
20 should be noted that in the case of mobile telephones, for example, there is no ground in the classical sense, but instead only a local ground, which essentially plays the role of a

reference potential but may not be considered an infinitely good diversion for charge carriers.

In one embodiment of the circuit arrangement, a protection device is provided
5 whose insertion loss is less than 0.3 dB. Such a protection device has the advantage that processing of the useful signals in the circuit arrangement is hardly interfered with, so that, for example, when the mobile telephone is used, it is possible to dispense with the use of unnecessarily powerful amplifiers, which can improve both the standby operating time and the transmission quality of the information.

10 In one embodiment of the circuit arrangement, the voltage limitation element has a capacitance lower than 1 pF. This can make the parasitic capacitance of the voltage limitation element low enough to prevent an interference of the useful signals or the useful signals from being too strongly attenuated.

15 For example, a gallium arsenide double diode is suitable as a voltage limitation element.

Such a double diode may be integrated in the circuitry of the circuit arrangement
20 in such a way that the primary protection device contains a lead that connects the terminal to the switching unit. The lead is connected to the reference potential via the

voltage limitation element. This results in the voltage limitation element being connected virtually in parallel to the signal lead:

5 In another embodiment of the circuit arrangement, a second protective element is connected in parallel to the first voltage limitation element. In this case, it is advantageous if a capacitor is also connected to the lead in series between the protective elements.

10 The second protective element may be, for example, a spark gap. The use of a spark gap is in particular advantageous because it can be easily integrated in a ceramic multilayer substrate, making it possible to improve the integration level of the circuit arrangement advantageously. The spark gap is in particular suitable if the switching unit contains pin diodes.

15 Combining the first voltage limitation element with a second protective element makes it possible to subdivide the protective effect of the protection device into a coarse protection and a fine protection. If a gallium arsenide double diode is used for the first voltage limitation element, this may be considered a fine protection for the circuit arrangement. Accordingly, the second protective element may assume the function of the
20 coarse protection. In this connection, the term coarse protection refers to currents of up to 30 A being capable of flowing through the relevant protective element. Accordingly,

lower currents should flow through the voltage limitation element that represents the fine protection.

In another embodiment of the circuit arrangement, the second protective element
5 may be a polymer suppressor. In this polymer suppressor, an additional electrically
conductive polymer is embedded in a spark gap, the polymer changing its electrical
conductivity with the applied voltage.

In another embodiment of the circuit arrangement, the second protective element
10 may be an overvoltage component whose parasitic capacitance does not exceed 1 pF.
Such a limit for the capacitance is advantageous because it simultaneously limits the
maximum insertion loss for the second protective element.

A varistor may be considered, for example, as an overvoltage component.
15

In another embodiment of the circuit arrangement, an inductive element is
provided as the second protective element. It is advantageous if the inductive element is
greater than 18 nH. In this case, the second protective element has an adequately low
insertion loss.

20 In addition to the gallium arsenide double diode, any other overvoltage
component whose parasitic capacitance is lower than 1 pF and has a switching voltage

lower than 200 V is suitable for the first voltage limitation element. Advantageously, a first voltage limitation element with a switching voltage lower than 100 V is used. The lower the switching voltage of the first voltage limitation element, the more effectively can sensitive components of the circuit arrangement be protected against excessively high voltage pulses. However, it must be considered that the useful signals that are used by the circuit arrangement, for example, to transfer information or speech must also have a specific minimum signal elevation, which may, of course, not be included the switching voltage of the first voltage limitation element in order to prevent the transmission of the signals or speech from being deteriorated by the second protective element.

10

Furthermore, an overvoltage component whose insertion loss is lower than 0.3 dB may be considered as a first voltage limitation element.

In another embodiment of the circuit arrangement, one or a plurality of control leads is provided to control the switch position of the switching unit. Each control lead is advantageously, but not necessarily, connected to a secondary protection device against high voltages.

This embodiment of the circuit arrangement has the advantage that it is also possible to effectively suppress interference from the control leads. In addition to the interference pulse entering the circuit arrangement directly via the terminal, an electrostatic discharge may also generate a high voltage at the circuit arrangement via a

ground coupling or via a coupling through the common reference potential. This may result, for example, from the control input ordinarily used in a switch being either at a high potential or at a low potential. The high potential is defined in that it is, for example, 2.3 V higher than the ground potential of the circuit arrangement. Because, in a mobile telephone, just as in many other devices based on signal transmission using antennas, the signal insertion passes from the antenna to the system ground, in the case of a circuit arrangement mentioned above, it is also possible to cause an electrostatic discharge directly to the ground potential of the circuit arrangement. Via the direct coupling of a control lead to ground via the condition "high," the voltage pulse arising from an electrostatic discharge may also act on the circuit arrangement via the control lead in addition to the path via the antenna.

In one embodiment of the circuit arrangement, a supply lead is provided for an operating voltage. The supply lead is advantageously, but not necessarily, connected to a secondary protection device against electrostatic discharges. That which has already been described above concerning the control leads also applies to possible interference potentials at the supply lead.

In a special embodiment of the circuit arrangement, two field effect transistors are provided in the switching unit. Each break distance of each field effect transistor connects the terminal to a signal lead. Each gate of each field effect transistor is

connected to a control lead. Furthermore, each gate is connected to a secondary protection device against electrostatic discharges.

Each of the cited secondary protection devices may include a voltage limitation
5 element having a switching voltage lower than 100 V. A varistor or also a Zener diode, for example, may be considered for this purpose.

In order to facilitate the diversion of interference impulses to the common reference potential, the particular voltage limitation elements of the secondary protection
10 devices are connected to the reference potential.

A circuit arrangement in which the terminal is the antenna input of a mobile telephone is cited as a special embodiment of the circuit arrangement. The use of the circuit arrangement in a mobile telephone is suggested in particular in this case.
15

To this end, it is also advantageous if the signal leads form the transmission/reception paths of a mobile telephone.

It is furthermore advantageous if the switching unit includes a gallium arsenide
20 switch. Such gallium arsenide switches are in particular favorable with regard to power consumption, as well as being very fast.

In another embodiment of the circuit arrangement, the switching unit and the primary protection device as well as the secondary protection device, if necessary, may be integrated in a multilayer ceramic substrate. This can increase the integration level of the circuit arrangement very effectively, which is of advantage in mobile applications in
5 mobile radio telephony in particular.

The present invention will be explained in greater detail below based on exemplary embodiments and the associated figures.

10 Figure 1 shows an example of a circuit arrangement in a schematic representation.

Figure 2 shows an exemplary embodiment of the primary protection device in a schematic representation.

Figure 2a shows another exemplary embodiment of the primary protection device
15 in a schematic representation.

Figure 3 shows another example of the circuit arrangement in a schematic representation.

Figure 4 shows another example of the circuit arrangement in a schematic representation.

20 In the figures, it should be noted that elements that are similar to one another or perform the same function are denoted by identical reference symbols.

Figure 1 shows a circuit arrangement having a terminal 1, which is suitable as an input or output for a high-frequency signal. In addition, a switching unit 3 is provided, which optionally connects terminal 1 to one of signal leads 21a, 21b, 21c, 22a, 22b.

5 A primary protection device 41 connected to reference potential 7 is provided between terminal 1 and switching unit 3. Control leads 91, 92, 93 are provided for the control of switching unit 3. Control voltages VC1, VC2, VC3 are applied to control leads 91, 92, 93. Each of control leads 91, 92, 93 is connected to a secondary protection device 42. Each of protection devices 42 is connected to reference potential 7. Secondary
10 protection devices 42 are formed by voltage limitation elements 53a, 53b, 53c. These may be, for example, varistors or Zener diodes. The use of a multilayer varistor having a switching voltage lower than 100 V is considered in particular. Furthermore, a supply
lead 100 is provided which supplies operating voltage VCC to switching unit 3 and is also provided with a secondary protection device 42 in the form of a voltage limitation
15 element 54. Secondary protection devices 42 are needed in particular when switching unit 3 contains a gallium arsenide switch. In the event that switching unit 3 contains 3 PIN diodes, secondary protection devices 42 may be dispensed with. Signal leads 21a, 21b, 21c may, for example, be assigned to the Rx paths for the reception of signals of a mobile telephone. Accordingly, signal lines 22a, 22b in the Tx path may be assigned for
20 sending radio telephony signals of a mobile telephone.

Switching unit 3 may also be a ceramic multilayer component containing a gallium arsenide switch and, in addition, passive components for implementing frequency filters.

5 External terminal 1 may be used as a high-frequency signal input or as a high-frequency signal output. Particular consideration is given to conducting the GSM signals typically used in mobile telephones into switching unit 3 via external terminal 1 or to send them from switching unit 3 to the outside via terminal 1.

10 Figure 2 shows a detailed representation of primary protection device 41 of Figure 1. It is indicated here, for example, how primary protection device 41 may be designed. In the example of Figure 2, primary protection device 41 has a lead 6. Lead 6 connects terminal 1 to switching unit 3. Lead 6 is connected to a voltage limitation element 51. In the example of Figure 2, it is a gallium arsenide double diode and is designed as an npn
15 diode. Such a diode has the advantage that it can be manufactured to have a switching voltage lower than 100 V. Furthermore, it is possible to manufacture such double diodes to have an insertion loss less than 0.1 dB. Furthermore, it is possible to manufacture double diodes of this type to have a parasitic capacitance less than 0.1 pF. Since,
20 according to Figure 2, voltage limitation element 51 is in a parallel circuit to lead 6, the total capacitance of voltage limitation element 51 contributes to the parasitic capacitance. Voltage limitation element 51 is connected to reference potential 7. Furthermore, it is also conceivable to use a pnp double diode instead of the npn double diode.

In particular, a gallium arsenide double diode having a switching voltage between 30 and 100 V is used as voltage limitation element 41.

5 According to Figure 2A, another embodiment is shown for primary protection device 41. In addition to voltage limitation element 51, another protective element 52 is provided. It is now not necessary for protective element 52 to be a component having a very low resistance above a switching voltage and a very high resistance below the switching voltage. The protective element can also perform its protective function as a
10 current limitation. The protective function of component 52 is based on the fact that it connects lead 6 to reference potential 7, as a result of which a finite flow of current is connected between terminal 1 and reference potential 7 in any case. Consequently, it may also be assumed here that protective element 52 limits the current load of voltage limitation element 51. Protective element 52 may be, for example, a spark gap; see also
15 Figure 4 in this regard. However, it may also be a polymer suppressor. Furthermore, a coil is taken into consideration, the inductance of which is greater than 18 nH. In these cases, it is advantageous, in addition to the two protective elements 51, 52, also to connect a capacitor 8 between the two protective elements 51, 52 in series to lead 6.

20 This results in an LC component that is formed from the coil and capacitor 8. By an adequate selection of the capacitance, which should be greater than 22 pF and is 47 pF in one exemplary embodiment of the circuit arrangement, it is possible for the LC

component to have an insertion loss less than 0.1 dB, which is in the frequency range of interest today for mobile radiotelephony. In the exemplary embodiment shown in Figure 2A, protective element 52 has an inductance of 56 nH in the form of an inductor (see also Figure 3). In one embodiment of the circuit arrangement according to Figure 2, in conjunction with Figure 1, it is possible to specify a circuit arrangement that is RF-compatible for frequencies between 0 and 2 GHz and which simultaneously, according to Standard IEC61000-4-2, can effectively suppress ESD pulses up to 8 kV to below the error threshold of the components shown otherwise in the figures.

10 Voltage limitation elements 53a, 53b, 53c, 54 should have a switching voltage lower than 100 V. Preferably, they have a switching voltage that is lower than 10 V. The switching voltages may be selected here to be lower than in terminal 1 because the voltages occurring here normally do not significantly exceed the customary operating voltages of 5 V. In contrast, voltages of up to 30 V can occur at terminal 1 when a
15 mobile telephone is operated.

Figure 3 shows another embodiment for a circuit arrangement in which a protective element 52, which is a coil, is provided for primary protection device 41. This makes it simple to implement the inductance already discussed in Figure 2A.

20

Furthermore, Figure 3 shows the internal structure of an exemplary switching unit

3. Two field effect transistors 111, 112 are provided. Each field effect transistor 111,

112 has a break distance 121, 122, which can be switched through or also blocked via the corresponding gate 131, 132. Break distance 121, 122 connects terminal 1 to a signal lead 21a, 22a. Each gate 131, 132 is connected to a control voltage VC1, VC2.

Furthermore, each gate 131, 132 is connected to a secondary protection device 42, which, 5 in the example of Figure 3, is a varistor as the voltage limitation element. Each of the varistors is for its part connected to reference potential 7. It may also be seen from Figure 3 that instead of the npn double diode of Figure 2 and Figure 2A, the use of a pnp double diode in primary protection device 41 is also considered.

10 Field effect transistors 111, 112 that are shown in Figure 3 may also be, for example, a gallium arsenide switch. Accordingly, it is advantageous if field effect transistors 111, 112 are designed based on gallium arsenide.

Furthermore, the exemplary switching unit may also contain more than two field 15 effect transistors. In this case, each gate of each transistor must be connected to a secondary protection device.

With a maximum signal voltage of approximately 30 V generated in the mobile telephone when double diodes are used, the characteristics of the diode itself make a 20 switching voltage of approximately 30 to 60 V necessary.

Figure 4 shows another embodiment of the circuit arrangement in which a spark gap is selected for protective element 52. Also, when a spark gap is used, it is advantageous in addition to provide a capacitor 8, which is connected to line 6 according to Figure 2A and Figure 3. The use of a spark gap as protective element 52 is

5 advantageous in particular when PIN diodes are provided in switching unit 3 to switch the signals from terminal 1 to signal leads 21a, 21b, 21c, 22a, 22b.

The described invention is not limited to mobile telephones; instead, it may be used in any circuit arrangement in which high-frequency signals occur and in which a

10 protection against electrostatic discharges is necessary.

List of Reference Symbols

	1	Terminal
5	21a, 21b, 21c, 22a, 22b	Signal lead
	3	Switching unit
	41	Primary protection device
	42	Secondary protection device
	51, 53a, 53b, 53c, 54	Voltage limitation element
10	52	Protective element
	6	Lead
	7	Reference potential
	8	Capacitor
	91, 92, 93	Control lead
15	100	Supply lead
	VCC	Operating voltage
	VC1, VC2, VC3	Control voltage
	111, 112	Field effect transistor
	121, 122	Break distance
20	131, 132	Gate

Patent Claims

1. A circuit arrangement

- having a terminal (1) for a high-frequency signal,

5 - having at least two additional signal leads (21a, 21b, 21c, 22a, 22b) which form transmission/reception paths,

- having a switching unit (3) for connecting the terminal (1) to a signal lead (21a, 21b, 21c, 22a, 22b),

10 - having a primary protection device (41) against electrostatic discharges which is connected between the terminal (1) and the switching unit (3),

- wherein the primary protection device (41) contains a first protective element (51) which diverts all voltage pulses whose pulse height exceeds 200 V to reference potential (7),

15 - wherein the first protective element (51) has a capacitance (8) which is less than 1 pF,

- wherein the first protective element (51) has an insertion loss which is less than 0.3 dB,

- wherein the switching unit (3) and the primary protection device (41) are integrated in or on a multilayer substrate.

20

2. The circuit arrangement as recited in claim 1,

wherein the first protective element (51) is a gallium arsenide double diode.

3. The circuit arrangement as recited in claim 1 or 2,
wherein the primary protection device (41) contains a lead (6), which connects the
terminal (1) to the switching unit (3) and wherein the first protective element (51)

5 connects the lead (6) to the reference potential (7).

4. The circuit arrangement as recited in one of claims 1 through 3,
wherein a second protective element (52) is connected in parallel to the first
protective element (51).

10

5. The circuit arrangement as recited in claim 4,
wherein a capacitor (8) is connected in series to the lead (6) between the first
protective element (51) and the second protective element (52).

15

6. The circuit arrangement as recited in one of claims 4 or 5,
wherein the second protective element (52) is a spark gap.

7. The circuit arrangement as recited in one of claims 4 or 5,
wherein the second protective element (52) is a polymer suppressor.

20

8. The circuit arrangement as recited in one of claims 4 or 5,

wherein the second protective element (52) is an overvoltage component whose capacitance is smaller than 1 pF.

9. The circuit arrangement as recited in one of claims 4 or 5,

5 wherein the second protective element (52) is an inductance greater than 18 nH.

10. The circuit arrangement as recited in one of claims 1 through 9,

- wherein one or a plurality of control leads (91, 92, 93) are provided for the control of the switching unit,

10 - wherein each control lead (91, 92, 93) is connected to a secondary protection device (42) against electrostatic discharges.

11. The circuit arrangement as recited in one of claims 1 through 10,

15 wherein a supply lead (100) for an operating voltage (VCC) is provided, the supply lead being connected to a secondary protection device (42) against electrostatic discharges.

12. The circuit arrangement as recited in one of claims 1 through 11,

20 - wherein the switching unit (3) contains two field effect transistors (111, 112), the break distance (121, 122) of each field effect transistor (111, 112) connecting the terminal (1) to a signal lead (21a, 21b, 21c, 22a, 22b).

- wherein each gate (131, 132) of each field effect transistor (111, 112) is connected to a control lead (91, 92),

- and wherein each gate (131, 132, 93) is connected to a secondary protection device (42) against electrostatic discharges.

5

13. The circuit arrangement as recited in one of claims 10 through 12, wherein a secondary protection device (42) contains a protective element (53a, 53b, 53c, 54) having a switching voltage that is lower than 100 V.

10

14. The circuit arrangement as recited in one of claims 10 through 13, wherein a secondary protection device (42) contains a protective element (53a, 53b, 53c, 54) which is a varistor.

15

15. The circuit arrangement as recited in one of claims 10 through 13, wherein a secondary protection device (42) contains a protective element (53a, 53b, 53c, 54) which is a Zener diode.

20

16. The circuit arrangement as recited in one of claims 10 through 15, wherein the one or a plurality of secondary protection devices (42) is connected to the reference potential (7).

17. The circuit arrangement as recited in one of claims 1 through 16,

wherein the switching unit (3) contains PIN diodes.

18. The circuit arrangement as recited in one of claims 1 through 16,
wherein the switching unit (3) contains a gallium arsenide switch.

5

19. The circuit arrangement as recited in one of claims 1 through 18,
wherein the terminal (1) is the antenna input of a mobile telephone.

20. The circuit arrangement as recited in one of claims 1 through 19,
10 wherein the signal leads (21a, 21b, 21c, 22a, 22b) form transmission/reception
paths of a mobile telephone.

21. The circuit arrangement as recited in one of claims 1 through 20,
wherein the switching unit (3) and the primary protection device (41) are
15 integrated in a multilayer ceramic substrate.

Abstract of the Disclosure

Circuit Arrangement

5

A circuit arrangement is described having a terminal (1) for a high-frequency signal, having at least two additional signal leads (21a, 21b, 21c, 22a, 22b), having a switching unit (3) for connecting the terminal (1) to a signal lead (21a, 21b, 21c, 22a, 22b), having a primary protection device (41) against electrostatic discharges, which is
10 connected between the terminal (1) and the switching unit (3), the protection device (41) containing a first voltage limitation element (51), which diverts voltage pulses that exceed a switching voltage of 200 V to a reference potential (7). The use of a gallium arsenide double diode makes it possible to attain the advantage of a low insertion loss coupled with a low switching voltage.

15

Figure 1



P2002,0838

FIG 1

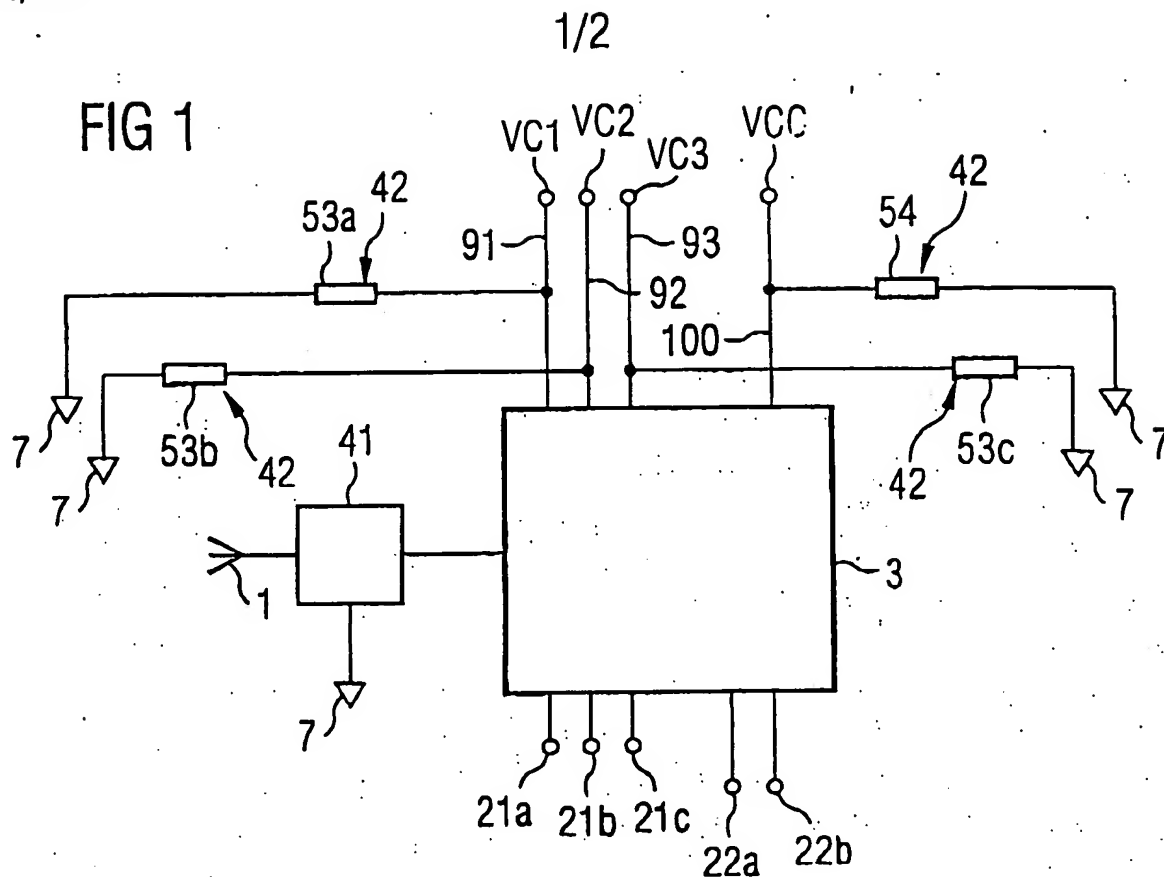


FIG 2

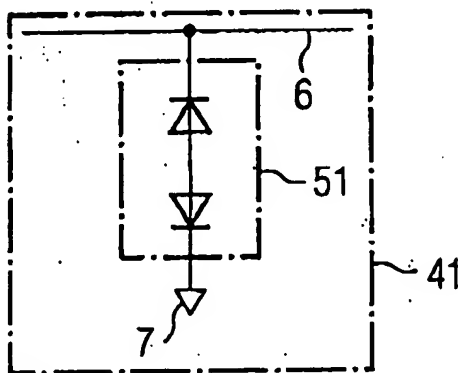
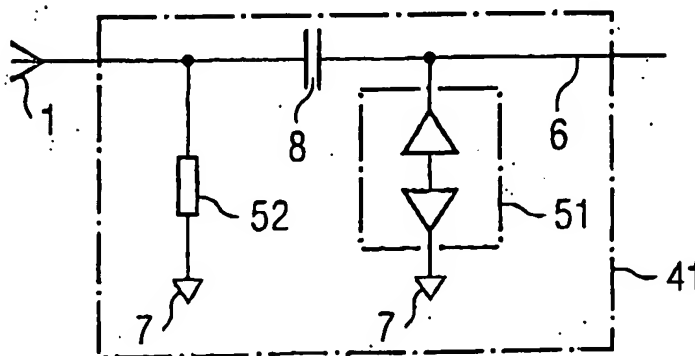
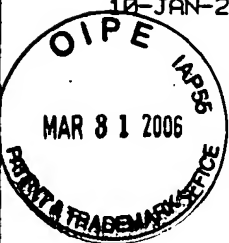


FIG 2A





P2002,0838

2/2

FIG 3

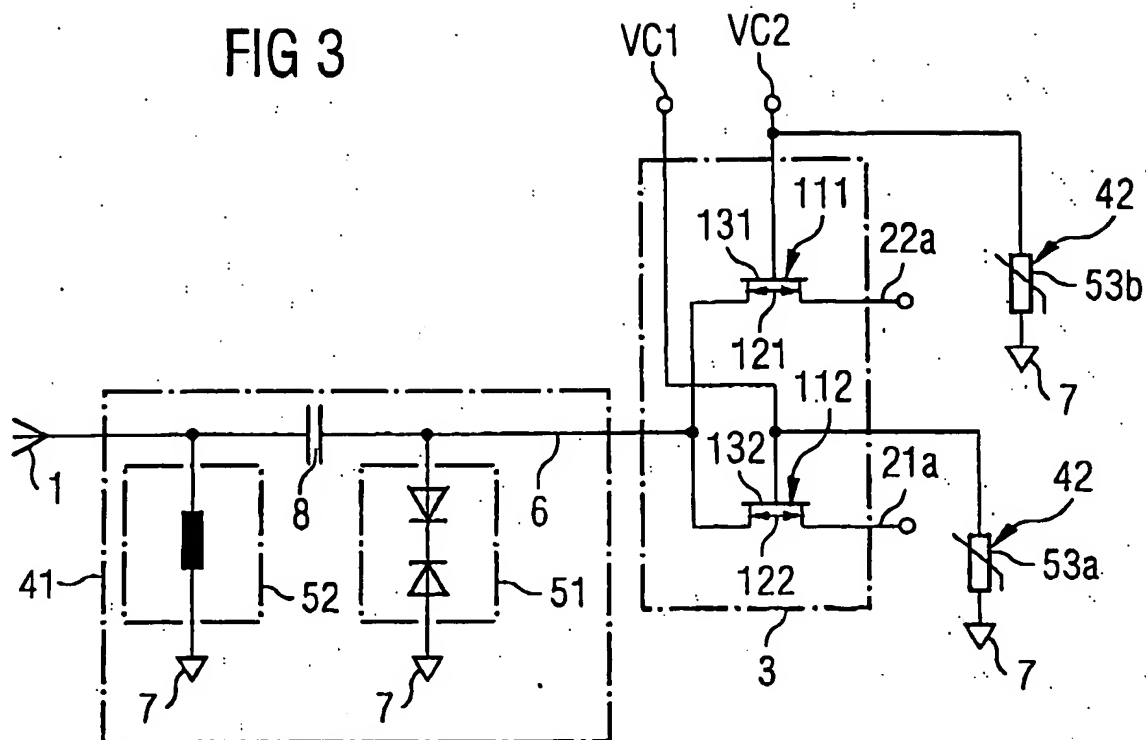


FIG 4

